

INVENTOR INFORMATION

10/019540  
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CORRESPONDENCE INFORMATION

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APPLICATION INFORMATION

Title Line One:: LAYERED STRUCTURE, METHOD FOR MANUFACTUR  
Title Line Two:: ING THE SAME, AND SEMICONDUCTOR ELEMENT  
Total Drawing Sheets:: 12  
Formal Drawings?: Yes  
Application Type:: Utility  
Docket Number:: 740819-724  
Secrecy Order in Parent Appl.?: No

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22204  
Registration Number One:: 38285  
Registration Number Two:: 28290  
Registration Number Three:: 35483  
Registration Number Four:: 45285  
Registration Number Five:: 26477

CONTINUITY INFORMATION

This application is a:: 371 OF  
> Application One:: PCT/JP00/04477  
Filing Date:: 07-06-2000

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 1-192659  
Filing Date:: 07-07-1999  
Country:: Japan  
Priority Claimed:: Yes

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